

SPECIFICATION FOR Near UV LED

3535 UVA LED PKG

DESCRIPTION

The AO35LV365S01X is a UV LED package based on Al2O3 with a peak wavelength of 365 nm.

This product is generally used as indicator and luminary for electronic equipment.

And it This product is mainly used as industrial UV curing and general



FEATURES

- Brilliant Aluminum SMT package with quartz glass
- Dimension (L x W x H) in mm
 - 3.5 x 3.5 x 2.0
- Forward current: up to 500 mA
- Radiant power (typ.)
 - 410 mW at 350 mA
- Viewing Angle (2θ1/2)
 - Typical 130°
- Built in ESD Protection device

APPLICATIONS

- UV Curing
- Moth killing
- Nail gel cure
- Counterfeit Detectors



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1. PKG Specifications

1) Absolute maximum ratings

[Ta=25°C]

Parameters	Symbol Value		Unit
Forward current	If	500	mA
Pulse Forward current*)	Ifp	700	mA
Reverse Voltage	Vr	5	V
Power dissipation	Pd	2.0	W
Operating temperature	Topr	- 30 ~ +105	°C
Storage temperature	Tstg	- 40 ~ +100	°C
Junction Temperature	Tj	125	°C
Soldering temperature	Tsol	260**)	℃
ESD Classification		Class2***)	

Notes

2) Electrical and Optical Characteristics

[Ta=25℃]

Parameter	Symbol	If	Min	Тур	Max	Unit
Peak wavelength	λр	350mA	365	368	370	nm
Dadient Flore	Φ.	100mA	-	120	-	mW
Radiant Flux	Фе	350mA	300	450	600	mW
Forward voltage	Vf	350mA	3.2	3.5	4.0	V
Full Width at Half Maximum	Δλ	350mA	9.4		nm	
Viewing angle	2θ1/2	350mA	130		o	
Thermal resistance	Rθ _{J-s}	350mA	7.0			°C/W

Notes

- Peak Wavelength(λp) Measurement tolerance is ±3nm.
- Radiant Flux(Φe) Measurement tolerance is ±10%.
- Forward Voltage(Vf) Measurement tolerance is $\pm 3\%$.
- Viewing angle(deg) Measurement tolerance is ±5deg.

^{*)} Pulse width \leq 0.1ms, Duty \leq 1/10

^{**)} Recommend JEDEC-J-STD-020D for reflow soldering.

^{***)} The ESD test follows JESD22-A114.



2. Bin Structures

[If=350mA, Ta=25℃]

Item	Bin	Min	Max	Unit
Peak Wavelength	W	365	370	nm
	R1	350	400	
	R2	400	450	
Radiant Flux	R3	450	500	mW
	R4	500	550	
	R5	550	600	
Forward Voltage	V1	3.2	3.4	
	V2	3.4	3.6	V
	V3	3.6	3.8	V
	V4	3.8	4.0	

Notes

- Peak Wavelength(λp) Measurement tolerance is ± 3 nm.
- Radiant Flux(Φe) Measurement tolerance is ±10%.
- Forward Voltage(Vf) Measurement tolerance is ±3%.

6



0

0

A035LV365S01X

3. Typical Characteristics Graphs

1) Forward Current vs. Forward Voltage

[Ta=25°C]

500
(W) 400

100

100

Forward Voltaget (V)

2) Relative Radiant Flux vs. Forward Current

[Ta=25°C]

1.5

(a)

No. 1.0

Relative Radiant Hink (a)

0.0

0 100 200 300 400 500

Forward Current (mA)

3) Forward Current Derating Curve

[Ta=25°C]

600

(Ye)
100

0

25

50

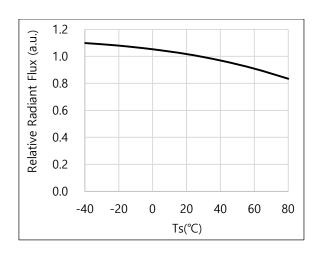
75

100

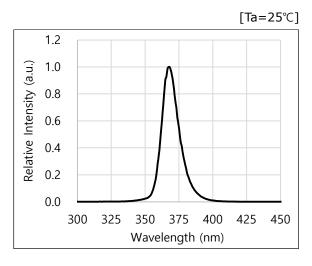
125

Ts(°C)

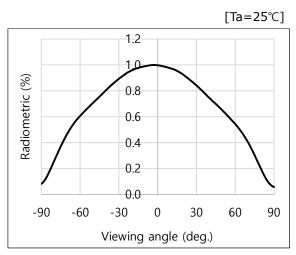
4) Ambient Temp. vs. Relative Radiant Flux



5) Spectrum



6) Typical Spatial Distribution





4. Reliability Test Items And Conditions

1) Reliability Test

Test Items	Reference	Test Conditions	Time
Room temperature Operating Life (RTOL)	JESD22 A108D	Ta=25℃, If=500mA	1000hr
High Temperature Storage Life (HTSL)	JEITA ED 4701 100 103	Ta=100°C	1000hr
Low Temperature Storage Life (LTSL)	JEITA ED 4701 200 202	Ta=-40°C	1000hr
High Temperature High Humidity Storage	JEITA ED 4701 100 103	Ta=60℃, RH90%	1000hr
Thermal Shock	MIL-STD 202G	-40°C(30min) ↔ 100°C(30min)	100 cycles
Temperature And Humidity Cyclic	JEITA ED 4701 200 203	-10℃~65℃, 0% ~ 90%RH 24hrs	10 cycles
Resistance to Soldering Heat	JEITA ED 4701 300 301	Tsol*)=260°C, 10secs	3 times

Notes

2) Failure Criteria

Test Items	Symbol	Test	Criteria For Ju	dging Damage	
rest Items	Symbol	Conditions	Min	Max	
Forward Voltage	Vf	If=350mA	-	Initial Value × 1.1	
Radiometric	Фе	If=350mA	Initial Value × 0.7	-	

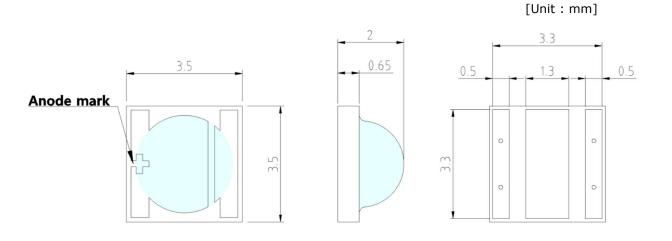
^{*)} Tsol-Temperature of tin liquid.



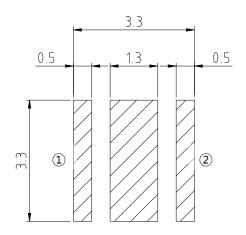
5. Outline Dimensions

1) PKG Dimensions

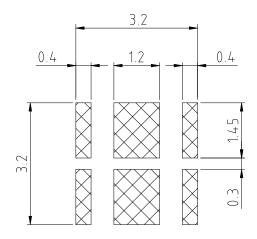
- PKG Dimensions 3.5 x 3.5 x 2.0 (L x W x H)
- Undefined tolerance is ±0.2mm



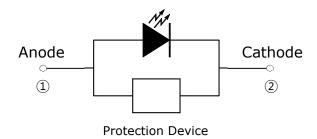
2) Recommended Solder Pad



3) Recommended Stencil Pattern

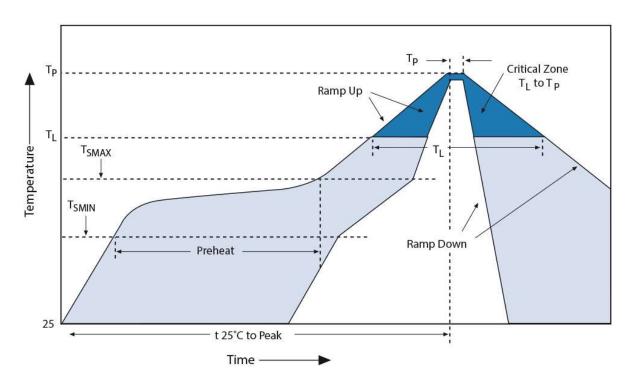


4) Internal Circuit





6. Reflow Soldering Profile



[Classification Reflow Profiles]

Profile Feature	Pb-free Assembly	Sn-Pb Eutectic Assembly
Average ramp-up rate (Ts _{max} to Tp)	3°C/second max	3°C/second max
Preheat - Temperature Min (Ts _{min}) - Temperature Max (Ts _{max}) - Time (Ts _{min} to Ts _{max}) (ts)	150 °C 200 °C 60-180 seconds	100 °C 150 °C 60-120 seconds
Time maintain above: - Temperature (T _L) - Time (t _L)	217 °C 60-150 seconds	183 °C 60-150 seconds
Peak Temperature (Tp)	260 ℃	235 ℃
Time within 5°C of actual Peak Temperature (tp)²	20-40 seconds	10-30 seconds
Ramp-down Rate	6 °C/second max	6 °C/second max
Time 25°C to Peak Temperature	8 minutes max.	6 minutes max.

Notes

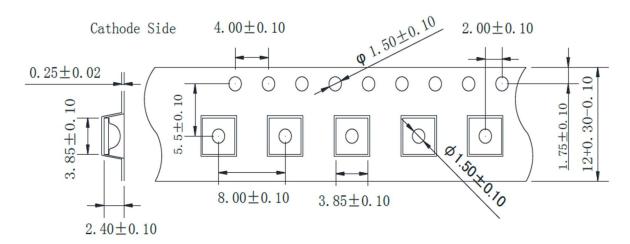
- All temperature refer to topside of the package, measured on the package body surface.
- The LED package is designed to be reflow soldered to a PCB. If dip soldered or hand soldered,
 PEC will not guarantee its reliability.
- Reflow soldering must not be done more than two times.
- When the LED PKG is cooled at the maximum temperature, a rapid temperature fall is not recommended.



7. Taping and Reel packing

1) Tape

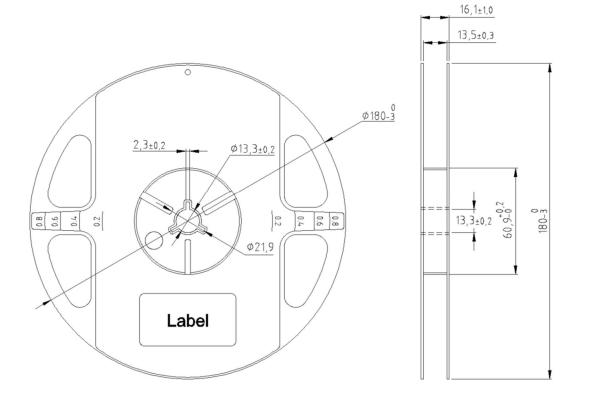
[Unit:mm]



2) Reel

PKG Quantity: 1,000 PCS/Reel

[Unit: mm]





8. Precautions on use

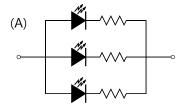
1) Storage condition

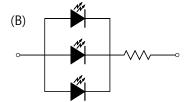
- This LED PKG is vacuum-packed in an aluminum bag containing a dehumidifying agent. However, if the storage environment is not good, the LED PKG can absorb moisture.
- When soldering with LED PKG absorb moisture, vaporization of moisture may occur and internal air may expand. This may cause the quartz to peel off or deteriorate its optical properties.
- It is recommended to keep in the environment shown in the table below.

Conditions		Temperature	Humidity	Time	
	Before Opening	5°C ~ 30°C < 50%RH		Within 1 Year from	
Storage	Aluminum Bag			the Delivery Date	
Storage	After Opening	5℃ ~ 30℃	< 60%RH	≤ 672 hours	
	Aluminum Bag		V 00701411	2 072 Hours	
	Baking		< 10%RH	10 ~ 24 hours	

2) Circuit design

- In designing a circuit, the current through each LED must not exceed the absolute maximum rating specified for each LED. In the meanwhile, resistors for protection should be applied, otherwise slight voltage shift will cause big current change, burn out may happen.
- It is recommended to use Circuit A which regulates the current flowing through each LED rather than Circuit B. When driving LEDs with a constant voltage in Circuit B, the current through the LEDs may vary due to the variation in Forward Voltage (VF) of the LEDs. In the worst case, some LED may be subjected to stresses in excess of the Absolute Maximum Rating.

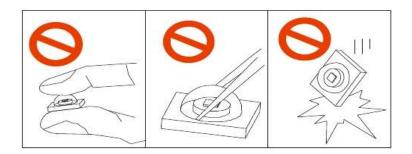






3) Handling precautions

- When handling the product, touching the encapsulant with bare hands will not only contaminate its surface, but also affect on its optical characteristic.
- Excessive force to the encapsulant might result in catastrophic failure of the LEDs due to die breakage or wire deformation. For this reason, please do not put excessive stress on LEDs, especially when the LEDs are heated such as during Reflow Soldering.



- The silicon resin of encapsulant is fragile, so please avoid scratch or friction over the epoxy resin surface. While handling the product with tweezers, do not hold by the epoxy resin, be careful.

4) ESD (Electrostatic Discharge)

- Use all objects and materials in the workplace for anti-static measures.
- Grounding
- ① All instruments, JIG, and equipment in the workplace should be grounded and measured once a month by a ground resistance meter.
- ② Workers should wear antistatic clothing and ground through a wrist strap or heel ground. Earth ring should be connected so as not to be shaken when grounding, and check for disconnection every day.
- The working surface of the work table is provided with a conductive mat and grounded. Periodically measure and check the conductivity state.
- The work table preferably has a surface resistance of 105 to 109 [Ω /SQ], and the metal work table is not good. (Breakage due to rapid discharge)
- Install conductive tiles or mats and regularly measure and manage grounding resistance and static electricity.
- Partially high static electricity is generated such as rotating body, TV, monitor, JIG. If it is non-conductive, use ionizer or anti-static spray regularly and prevent static electricity.
- In addition to general cases, use humidity control or partial humidifier to suppress the generation of static electricity.



5) Thermal Management

- Thermal management is closely related to the lifetime of LEDs.
- The temperature of the LED during operation must not exceed the junction temperature (Tj).
- For temperature management of the LED, the thermal resistance of the PCB and the spacing between the LEDs must be considered.
- For PCBs, a Cu based metal PCB is recommended, and if necessary, a heat sink should be attached to manage the LED temperature.

6) Eye Safety

- This UV LED PKG emits high power UV light.
- Exposure to light with strong UV light can cause damage to the human eye and skin.
- Do not look directly or indirectly at UV light.
- If exposure to UV light is unavoidable, the body should be protected by suitable protection devices such as goggles and clothing.
- Attach the following warning labels on products/systems that use UV LEDs.



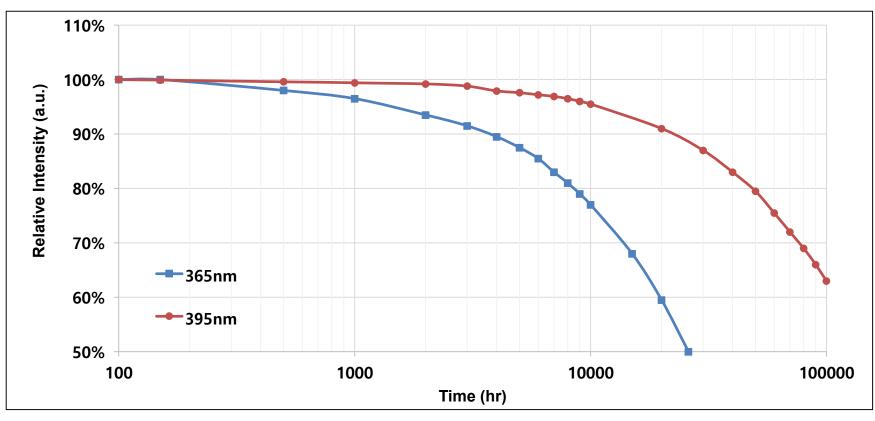


3535UVA PKG lifetime Extrapolation





BR35QVXXXL01X / BR35LVXXXL01X Series

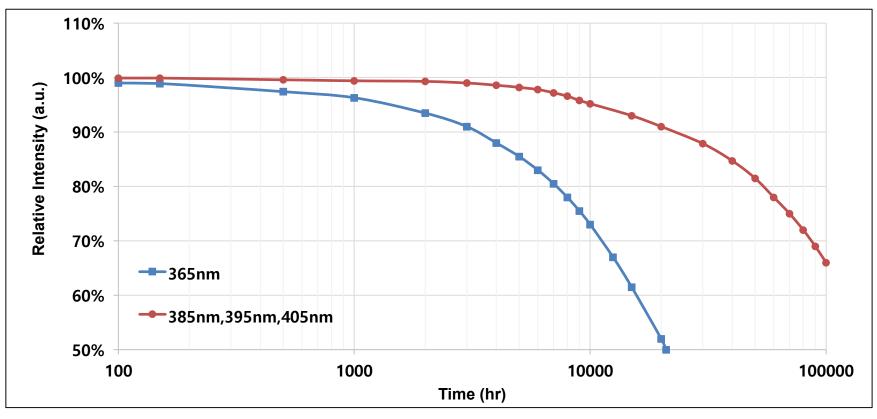


No.	Wp(nm)	If(mA)	Ta(℃)	Tj(℃)	Calculated L70	Calculated L80
1	365	500	50	75	~ 15,000	~ 9,000
2	385, 395, 405	500	50	71	~ 70,000	~ 40,000

^{*} All characteristics shown are for reference only and are not guaranteed.



AO35LLXXXX01X / AO35LVXXXX01X Series



No.	Wp(nm)	If(mA)	Tj(℃)	Calculated L70	Calculated L80
1	365	350	60	~ 10,000	~ 7,000
2	385, 395, 405	350	60	~ 80,000	~ 50,000

^{*} All characteristics shown are for reference only and are not guaranteed.

